

REMARKS/ARGUMENTS

Claims 1-3, 5-23 and 25-30 are pending in the application. By this Amendment, claims 26-30 are added, claims 7 and 22 are amended and claim 24 is cancelled without prejudice or disclaimer. It is believed no new matter is introduced in the application. Support for the claims can be found throughout the original specification, including the claims and drawings originally filed. Reconsideration of the application in view of the above amendments and at least the reasons below is respectfully requested.

Applicant sincerely acknowledges the Office Action's indication that claims 1-3, 5-11 and 21-25 are in condition for allowance. However, for at least the reasons set forth below, Applicant respectfully submits that all pending claims are in condition for allowance.

A. The Office Action rejects claims 12-13, 16-17 and 19 under 35 U.S.C. §102(e) over U.S. Patent No. 6,564,332 to Nguyen et al. (hereafter Nguyen). Claim 14 is rejected under 35 U.S.C. §103(a) over Nguyen. The rejections are respectfully traversed.

With respect to claim 12, Applicant respectfully submits that Nguyen fails to disclose every claimed feature as required under §102. For example, Nguyen fails to disclose or suggest at least features of a clock generator and a clock adjustor operating in a

power source mode, said clock adjustor generating a second clock signal for the CPU and a third clock signal for the bridge controller, wherein the second and third clock signals are two distinct clock signals outputted by the clock adjustor and have frequencies that are independent of each other and combinations thereof as recited in claim 12.

The Office Action asserts a clock synchronizer 311 generates a second clock signal for a CPU and a third clock signal for a bridge controller, citing column 6, lines 29-35 and Figure 3 of Nguyen. In contrast, Applicants respectfully submit that the clock synchronizer 311 in Nguyen generates two clock signals for a processor 310. Nguyen discloses a pipeline 313 of the processor 310 operates at a core frequency (asserted second clock signal) that can be decreased to a reduced core frequency in accordance with a throttle signal by using a third ratio to generate the reduced core frequency or stepping through various multiplication ratios between the high and low core frequencies. See column 6, lines 40-44 and 63-66; column 8, lines 7-30 and Figure 5 of Nguyen. Alternatively, Nguyen discloses the processor 310 power consumption can be reduced by stalling a portion of the pipeline 313 or issuing no-ops to the pipeline 313. See column 7, lines 10-12 and 27-29 and Figures 6 and 7 of Nguyen.

However, Applicant respectfully submits that as described above, the bus interface 312 is part of the processor 310. Further, the bus frequency provided to bus interface 312 remains consistent.

Thus, Applicant respectfully submits that the clock synchronizer 311 does not generate a third clock signal for the bridge controller, wherein the second and third clock signals are two distinct clock signals outputted by the clock adjustor and have different frequencies that are independent of each other and combinations thereof as recited in claim 12. Further, Applicant respectfully submits Nguyen does not teach or suggest any modifications to its disclosure that would result in at least features of a clock generator and a clock adjustor and combinations thereof as recited in claim 12.

With respect to claim 13, Applicant respectfully submits that Nguyen does not teach or suggest at least features of the bridge controller and combinations thereof as recited. The Office Action asserts a bridge controller shown in Figure 1 controls a clock speed of a bus for data communication, however, in Item 3 on page 2 of the Office Action, the Office Action asserts the bridge controller is a bus interface 312 that is shown in Figure 3 of Nguyen within the processor 310. Thus, Applicant respectfully submits

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Nguyen does not teach or suggest any modification to its disclosure that would result in at least features of the bridge controller and combinations thereof as recited in claim 13.

With respect to claim 16 Applicant respectfully submit that a clock synchronizer 311 does not teach or suggest generating clock signals other than clock signals internal to the processor 310 described above. Thus, Applicant respectfully submits Nguyen does not teach or suggest the clock synchronizer 311 generates a fourth clock signal for the video processor, the fourth clock signal being distinct from the second and third clock signals and having a different frequency than the second and third clock signals and combinations thereof as recited in claim 16.

For at least the reasons set forth above, Applicant respectfully submits claim 12 defines patentable subject matter. Claim 19 defines patentable subject matter for at least reasons similar to claim 12. Claims 13-14 and 16-17 depend from claim 12 and therefore also define patentable subject matter for at least that reason as well as their additionally recited features. Withdrawal of the rejection of claims 12-13, 16-17 and 19 under §102 and claim 14 under §103 is respectfully requested.

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B. The Office Action rejects claims 15, 18 and 20 under 35 U.S.C. §103(a) over Nguyen over Nguyen and U.S. Patent No. 6,763,478 to Bui. The rejection is respectfully traversed.

With respect to claim 12, Applicant respectfully submits that Bui does not teach or suggest at least features of a clock generator and a clock adjustor and combinations thereof as recited in claim 12 and lacking from Nguyen. Thus, Applicant respectfully submits that Nguyen and Bui, individually or in combination, do not teach or suggest at least features of the clock generator and clock adjustor and combinations thereof as recited in claim 12.

For at least the reasons set forth above, Applicant respectfully submits claim 12 defines patentable subject matter. Claim 19 defines patentable subject matter for at least the reasons similar to claim 12. Claims 15, 18 and 20 depend from claims 12 and 19, respectively, and therefore also define patentable subject matter for at least that reason as well as their additionally recited features. Withdrawal of the rejection of claims 15, 18 and 20 under §103 is respectfully requested.

C. Claims 26-30 are newly added by this Amendment and are believed to be in condition for allowance.

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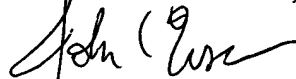
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CONCLUSION

In view of the foregoing amendments and remarks, it is respectfully submitted that the application is in condition for allowance. If the Examiner believes that any additional changes would place the application in better condition for allowance, the Examiner is invited to contact the undersigned attorney, Carl R. Wesolowski, at the telephone number listed below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,
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Date: June 27, 2005